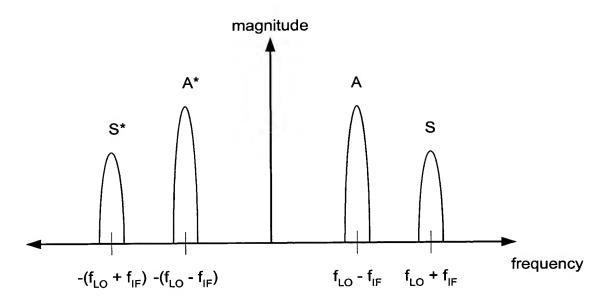
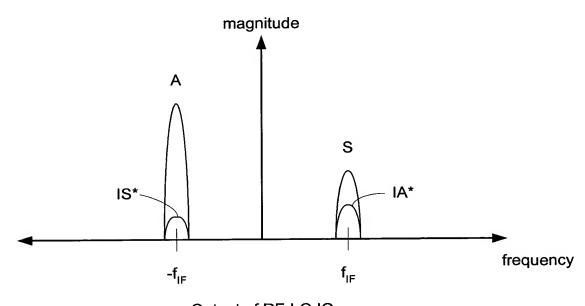


FIG. 1



Input to RF-LO IQ mixer



Output of RF-LO IQ mixer

FIG. 2

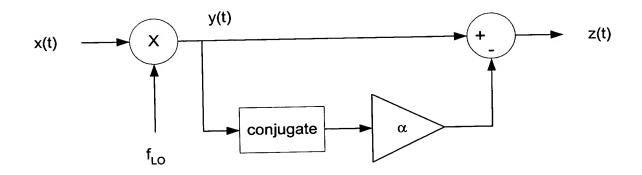


FIG. 3A

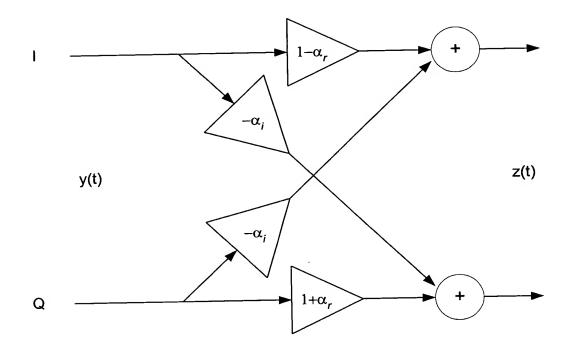


FIG. 3B

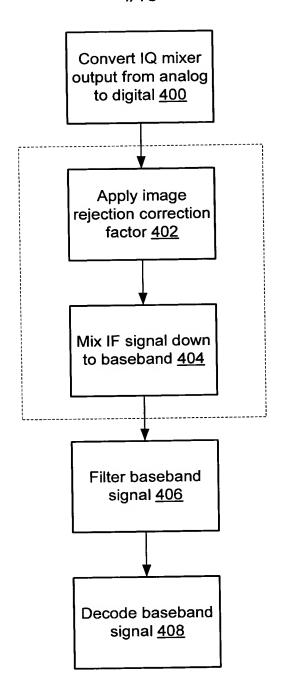


FIG. 4

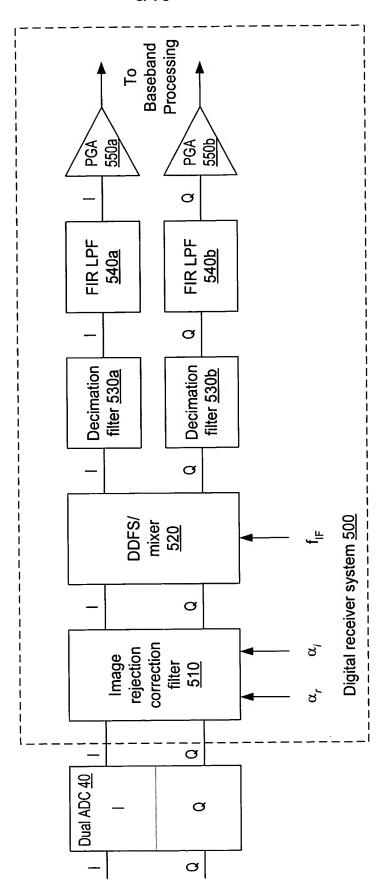


FIG. 5

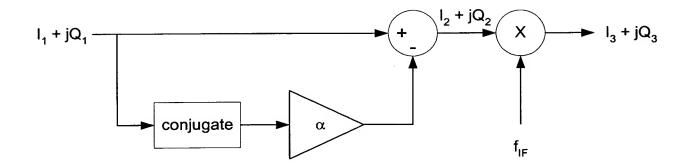


FIG. 6A

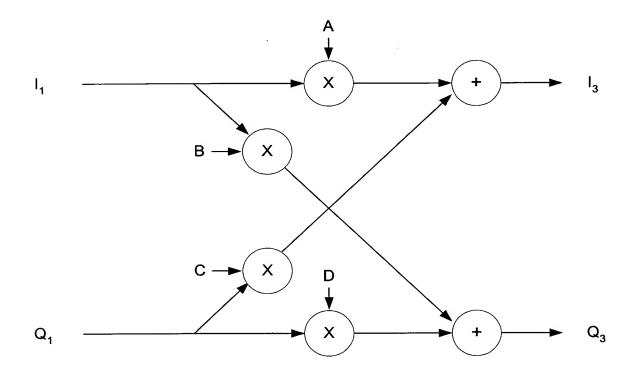


FIG. 6B

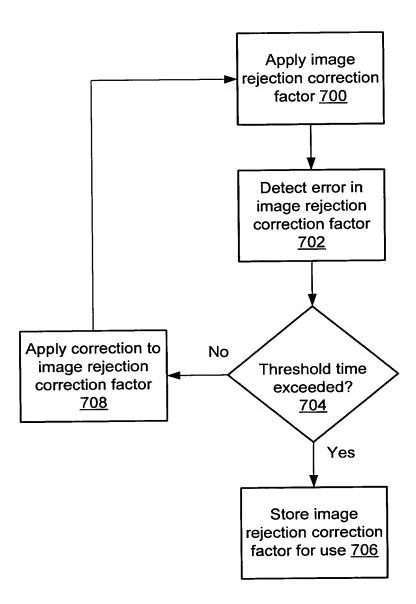
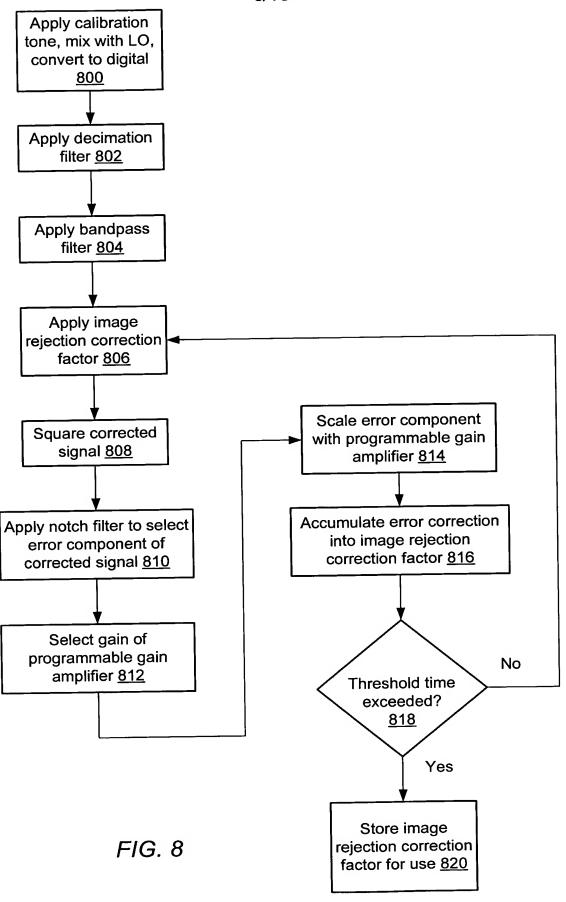
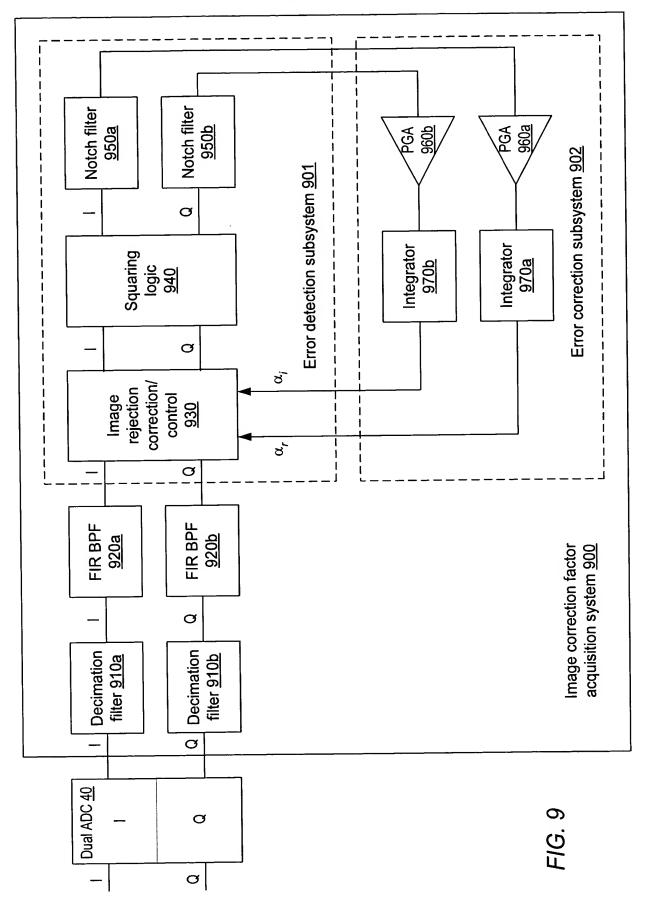
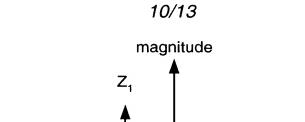


FIG. 7







 $Z_{1}$   $Z_{2}$   $Z_{3}$   $Z_{4}$   $Z_{3}$   $Z_{4}$   $Z_{3}$   $Z_{4}$   $Z_{5}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{2}$   $Z_{3}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{2}$   $Z_{3}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{2}$   $Z_{3}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{2}$   $Z_{3}$   $Z_{1}$   $Z_{3}$   $Z_{1}$   $Z_{2}$   $Z_{3}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{2}$   $Z_{1}$   $Z_{2}$   $Z_{2}$   $Z_{3}$   $Z_{1}$   $Z_{3}$   $Z_{1}$   $Z_{1}$   $Z_{1}$   $Z_{2}$   $Z_{3}$   $Z_{3$ 

Output of ADC with calibration tone input at LO mixer

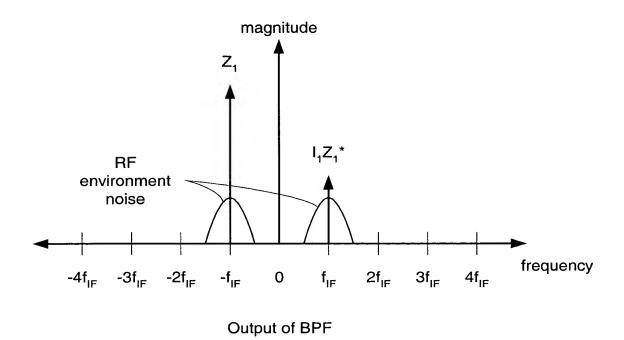
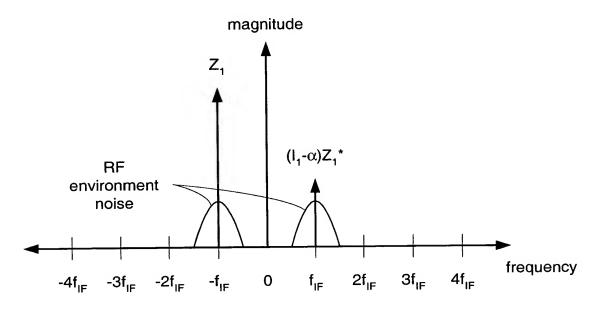
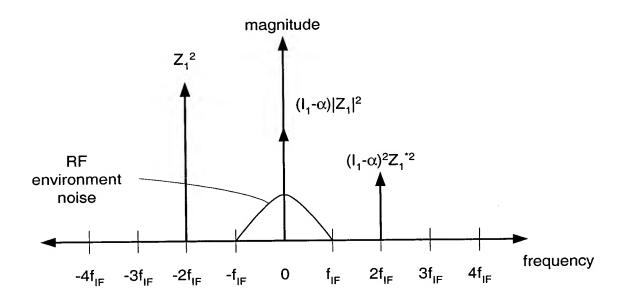


FIG. 10A





Output of image rejection correction filter



Output of squaring logic

FIG. 10B

